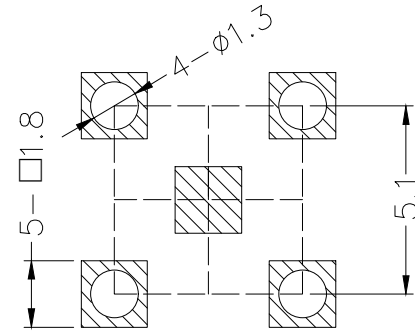
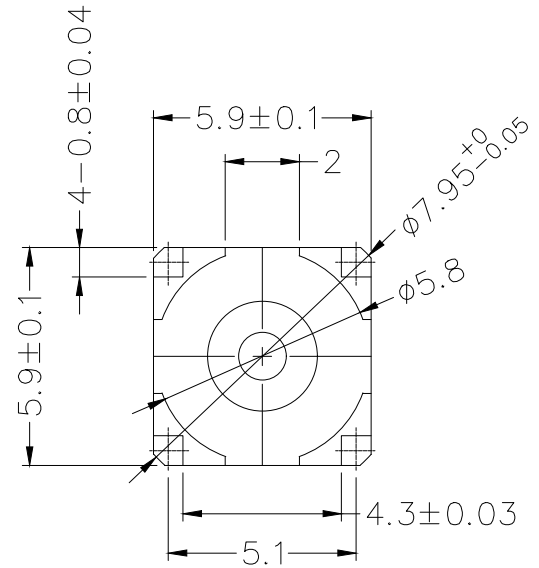
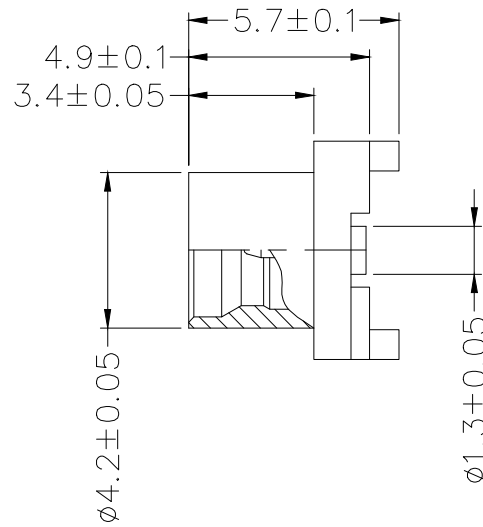


A
B
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D
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REV	LOCATIONS	DESCRIPTION	DATE	REVISER	APPD



Recommended P.C.Board Layout

SPECIFICATION

Technical Parameters

- Temperature range: -40~+125°C;
- Characteristic impedance: 50Ω;
- Ferquency range: DC~6GHz;
- Voltage Rating: 335V rms;
- Dielectric withstanding voltage: 1000V rms;
- Insulation resistance: ≥5000MΩ;
- VSWR: ≤1.3(DC~6GHz)
- Inner Conductor Contact Resistance: 5mΩ MAX;
- Outer Conductor Contact Resistance: 2.5mΩ MAX;

Mechanical

Durability: 500 cycles

Material and Plating

Insulators: PTFE(UL 94V-0), White
Contact: Brass, 0.025μm Gold plated
Body: Brass, 0.025μm Gold plated

	GENERAL TOLERANCES (UNLESS SPECIFIED)		APPROVE BY	DATE	PART NO.	ITEM NO.		
	X. ±0.30	X.* ±5°	CHECKED BY	DATE	F00SMP-P50P0SHS-01	F00SMP		
	DESIGN UNITS mm	X.X ±0.20	X.X* ±2°	GISELLE	04/JAN/22	TITLE	Building Technology Cornerstone	
	SCALE 5:1	X.XX ±0.15	X.XX* ±1°	DRAWN BY	DATE	SMP Plug 180° Vertical PCB Through Hole	REV 0	SHEET NO. 1/1
SIZE A4	X.XXX ±0.10	X.XXX* ±0.5°	PYD	04/JAN/22	THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO TXGA INDUSTRIAL ELECTRONICS(S.Z)CO.,LTD AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION			

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