



REV	LOCATIONS	DESCRIPTION	DATE	REVISER	APPD
		New drawing and add marking code	12/FEB/25	MATT	LEO

**SPECIFICATIONS**

**Electrical**

- 1.Current Rating: 3A AC(rms)/DC
- 2.Voltage Rating: 250V AC(rms)/DC
- 3.Contact Resistance: 25 mΩ Max
- 4.Insulation Resistance: 1000 MΩ MIN
- 5.Withstanding Voltage: 500V AC r.m.s
- 6.Temperature Range—Operating: -55°C~+105°C

**Material and Finish**

- 1.Housing: PBT UL 94V-0
- 2.Contact: Copper Alloy/Gold Flash Plated On Contact Areas over nickel
- 3.Shell: Spcc/Nickel Plated Over All
- 4.Screws: Copper Alloy/Nickel Plated Over All

\*Date Code: YYWW  
 Week  
 Year

**Ordering Information**

FDB B 02 — 15 FO S 3 XX XX K  
 1 2 3 4 5 6 7 8 9 10

1	Category FDB- D-SUB	2	Assembly Layout B-Bump	3	Distinction No. 02	4	Circuits 15	5	Type FO-Female
6	Entry Angle S-Vertical	7	Plating 3-Selective gold on contact area	8	Thickness of Gold Plating 00-Gold Flash 03-3u" ..... Custom Plate Available	9	Color-Resin K6-Black U1-Blue	10	Packaging K-Tray

 THIRD ANGLE PROJECTION	GENERAL TOLERANCES (UNLESS SPECIFIED)		APPROVE BY FRANK	DATE 12/FEB/25	PART NO. FDBB02-15FOS3XXXXK	ITEM NO. FDBB02	 Building Technology Cornerstone
	X.±0.30	X.±5'	CHECKED BY JACOB	DATE 12/FEB/25	TITLE D-SUB FEMALE 15Circuits BUMP TYPE CONN	REV 0	
DESIGN UNITS Metric	X.XX±0.20	.X'±2'	DRAWN BY MATT	DATE 12/FEB/25	THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO TXGA INDUSTRIAL ELECTRONICS(S.Z)CO.,LTD AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION		
SCALE 5:1	SIZE A4	X.XXX±0.10	.XX'±1'				