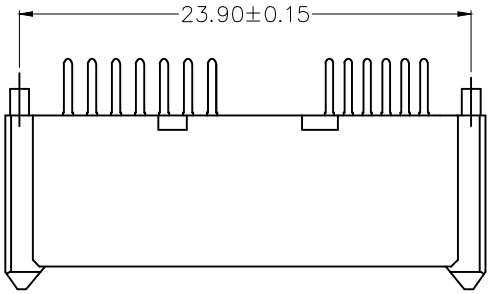
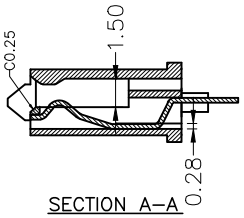
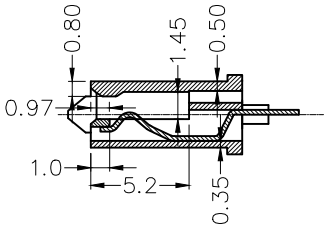
REV	LOCATIONS	DESCRIPTION	DATE	REVISER	APPD
1	1	Modify the tolerance	20/OCT/26	HJP	LEO

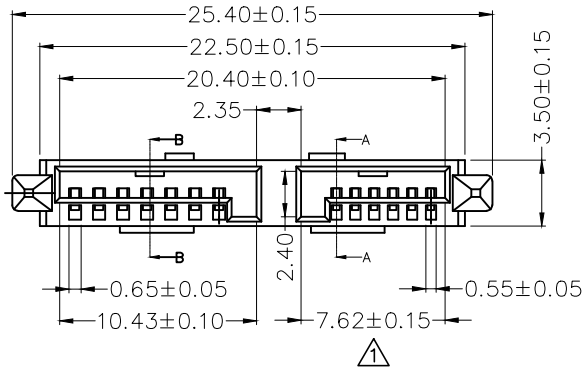


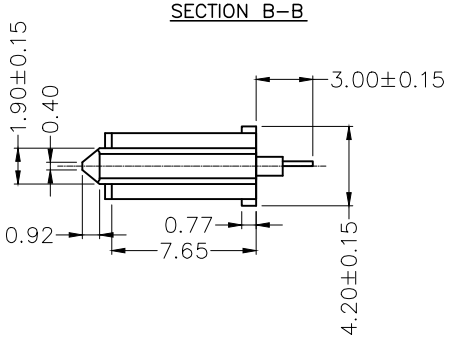


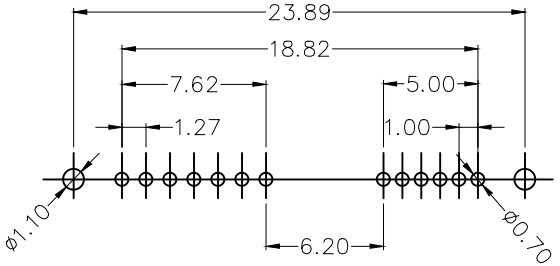
SECTION A–A



SECTION B–B

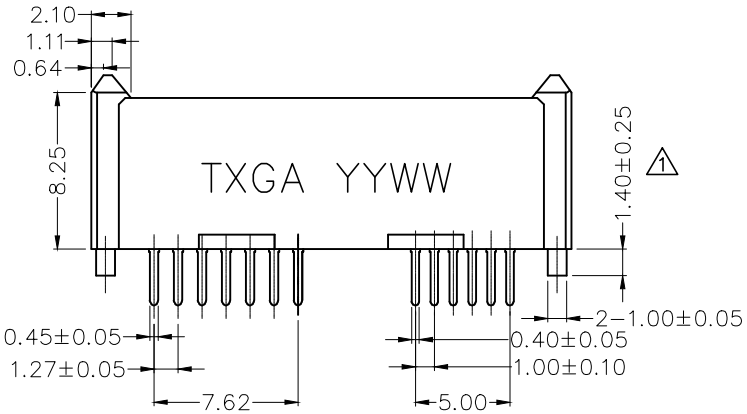






Recommended P.C.Board Layout

Tolerances: ±0.05



SPECIFICATIONS

Electrical
- Current Rating: 1.5A AC(rms)/DC
- Contact Resistance: 30 mΩ Max
- Insulation Resistance: 1000 MΩ MIN
- Withstanding Voltage: 500V AC r.m.s
- Temperature Range–Operating: −40°C~+85°C

Material and Plating
- Housing: Thermoplastic UL94V–0, Black
- Terminal: Phosphor Bronze /Gold Plating on Contact Area, 100u" Min tin Plating on Soldertails, 50u" Min Nickel under plating Over All.

Ordering Information

FST	13	02	—	F	D	S	3	XX	3	BK	K
1	2	3		4	5	6	7	8	9	10	11

1	Category	2	Circuits	3	Distinction No.	4	Type	5	Assembly Layout	6	Entry Angle
	FST–SATA		13–7+6circuits		02		F–Female		D–DIP		S–Vertical

7	Plating	8	Thickness of Gold Plating	9	Material–Resin	10	Color–Resin	11	Packaging
	3–Selective gold plated on contact area and tin on tails		01–1u" 05–5u" Custom Plate Available		3–LCP		BK–Black		K–Tray

THIRD ANGLE PROJECTION	GENERAL TOLERANCES (UNLESS SPECIFIED)		APPROVE BY	DATE	PART NO.	ITEM NO.	TXGA Building Technology Cornerstone
	X.±0.30	X.°±5°	LEO	17/NOV/21	FST1302–FDS3XX3BKK	FST1302	
DESIGN UNITS Metric(mm)	X.X±0.20	.X°±2°	CHECKED BY GISELLE	DATE 17/NOV/21	TITLE SATA 7+6P Female DIP		REV 1 / SHEET NO. 1/1
SCALE 2.5:1 / SIZE A4	X.XX±0.15	.XX°±1°	DRAWN BY MATT	DATE 17/NOV/21			
	X.XXX±0.10	.XXX°±0.5°					

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