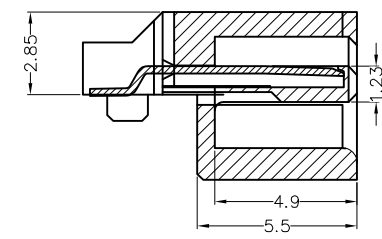
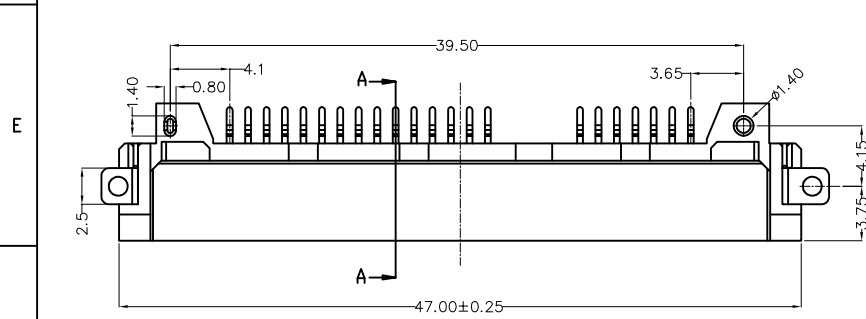
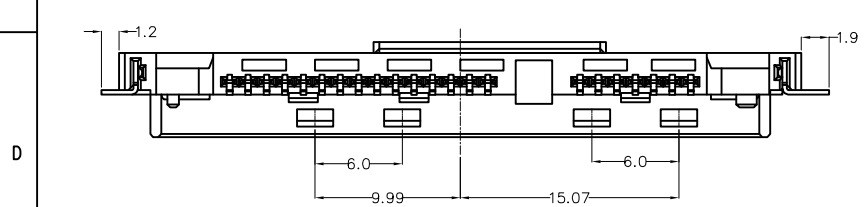
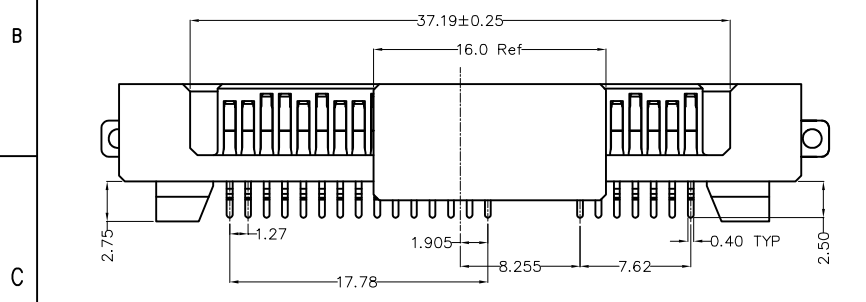
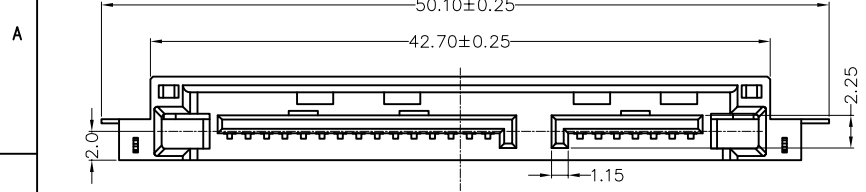
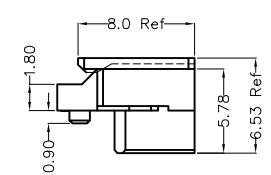


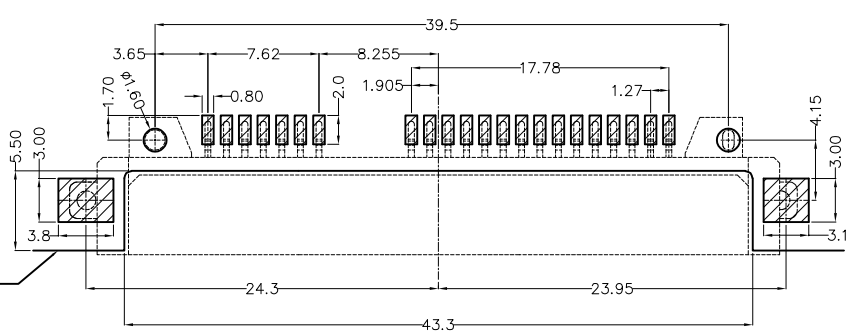
REV	LOCATIONS	DESCRIPTION	DATE	REVISER	APPD



SECTION A-A



PCB EDGE



Recommended P.C.Board Layout
General Tolerances: ±0.05mm

SPECIFICATIONS

Electrical
 Current Rating: 1.5A AC(rms)/DC
 Contact Resistance: 30 mΩ Max
 Insulation Resistance: 1000 MΩ Min
 Withstanding Voltage: 500V AC r.m.s
 Temperature Range-Operating: -40°C~+105°C

Material and Plating
 Housing: LCP+30%GF, UL 94V-0 Black
 Contact: Brass, T=0.25mm
 Plated: 15μ" Gold Plating On Contact Area
 100μ" Min. Tin-lead Plating On Solder Tails
 50μ" Min. Nickel Under Plating Overall

 THIRD ANGLE PROJECTION DESIGN UNITS METRIC(mm) SCALE 5:1 SIZE A4	GENERAL TOLERANCES (UNLESS SPECIFIED)		APPROVE BY	DATE	PART NO.	ITEM NO.	 Building Technology Cornerstone REV 0 SHEET NO. 1/1 THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO TXGA INDUSTRIAL ELECTRONICS(S.Z)CO.,LTD AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION
	X. ±0.30	X.' ±5'	CHECKED BY	DATE	FST2213-MSB3153BKM	FST2213	
	X.X ±0.20	X.X' ±2'	GISELLE	30/JAN/20	TITLE		
	X.XX ±0.15	X.XX' ±1'	DRAWN BY	DATE	SATA 7+15 MALE (SMT)		
X.XXX ±0.10	X.XXX' ±0.5'	MATT	30/JAN/20				