

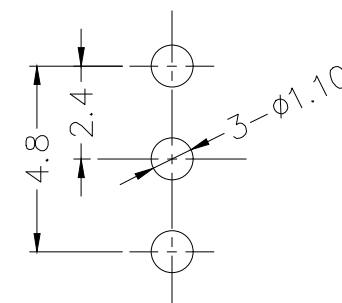
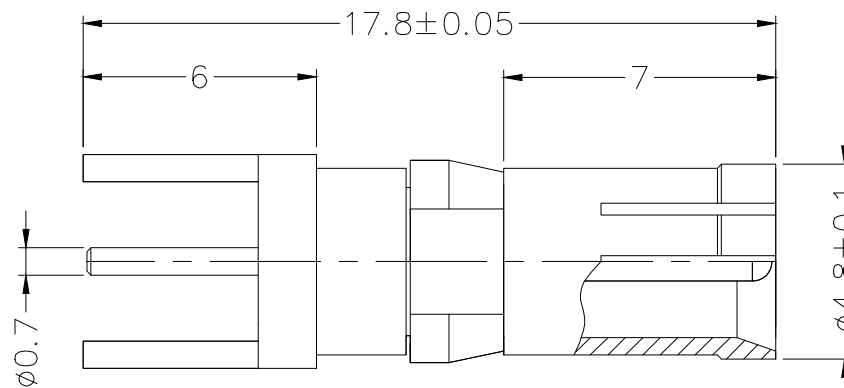
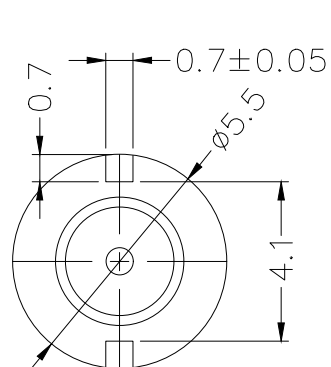
1 2 3 4 5 6 7 8

REV	LOCATIONS	DESCRIPTION	DATE	REVISER	APPD

A

B

C



Recommended P.C.Board Layout

SPECIFICATION

Technical Parameters

Temperature range: $-55\sim+125^{\circ}\text{C}$;
 Characteristic impedance: 50Ω ;
 Ferquency range: $\text{DC}\sim 2\text{GHz}$;
 Voltage Rating: 250V rms ;
 Dielectric withstanding voltage: 1000V rms ;
 Insulation resistance: $\geq 1000\text{M}\Omega$;
 VSWR: $\leq 1.06(\text{DC}\sim 0.1\text{GHz})$
 $\leq 1.22(\text{DC}0.1\sim 0.5\text{GHz})$
 $\leq 1.5(\text{DC}0.5\sim 2\text{GHz})$

Inner Conductor Contact Resistance: $10\text{m}\Omega \text{ MAX}$;

Outer Conductor Contact Resistance: $5\text{m}\Omega \text{ MAX}$;

Mechanical

Durability: 500 cycles

Material and Plating

Insulators: PTFE(UL 94V-0), White

Contact: Brass, Gold plated

Body: Brass, Gold plated

Snap Ring: Phosphor Bronze, NI plated

Mating End: Phosphor Bronze, Gold plated

FTDC 02 - F 50 P 1 XX S D
1 2 3 4 5 6 7 8 9

1	Category FTDC-D-Sub coaxial contact	2	Distinction No. 02	3	Gender F-Female	4	Impedence 50-500hm	5	Contact P-Pin
6	Plating 1-Gold Plated	7	Plating Thickness 01-0.025 μm 03-0.075 μm 04-0.125 μm 06-0.25 μm 07-0.375 μm 08-0.75 μm	8	Angle S-180°Vertical	9	Termination type D-Through Hole		

E

F

 THIRD ANGLE PROJECTION		GENERAL TOLERANCES (UNLESS SPECIFIED)		APPROVE BY LEO	DATE 04/JAN/22	PART NO. FTDC02-F50P1XXSD	ITEM NO. FTDC02	 Building Technology Cornerstone	
		X. ±0.30	X.* ±5°	CHECKED BY	DATE	TITLE D-Sub Coaxial Contact Jack 180° Vertical PCB Through Hole			
DESIGN UNITS mm		X.X ±0.20	X.X' ±2°	GISELLE	04/JAN/22			REV 0	SHEET NO. 1/1
SCALE 5:1	SIZE A4	X.XX ±0.15	X.XX' ±1°	DRAWN BY PYD	DATE 04/JAN/22	THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO TXGA INDUSTRIAL ELECTRONICS(S.Z)CO.,LTD AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION			
		X.XXX ±0.10	X.XXX' ±0.5°						

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