

REV	LOCATIONS	DESCRIPTION	DATE	REVISER	APPD
1	▲	Update the material of Bottom cover	10/OCT/24	MATT	LEO

SPECIFICATIONS

Electrical

Current Rating: 1.5A AC(rms)/DC
Voltage Rating: 30V AC(rms)/DC
Contact Resistance: 20 mΩ Max
Withstanding Voltage: 500V AC r.m.s
Insulation Resistance: 1000 MΩ Min
Temperature Range-Operating: -25°C~+85°C

Material

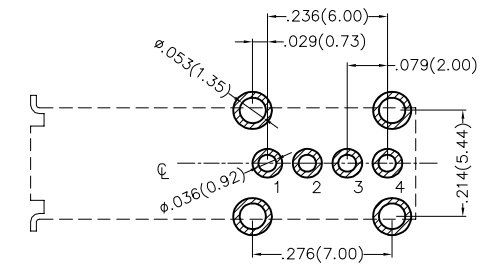
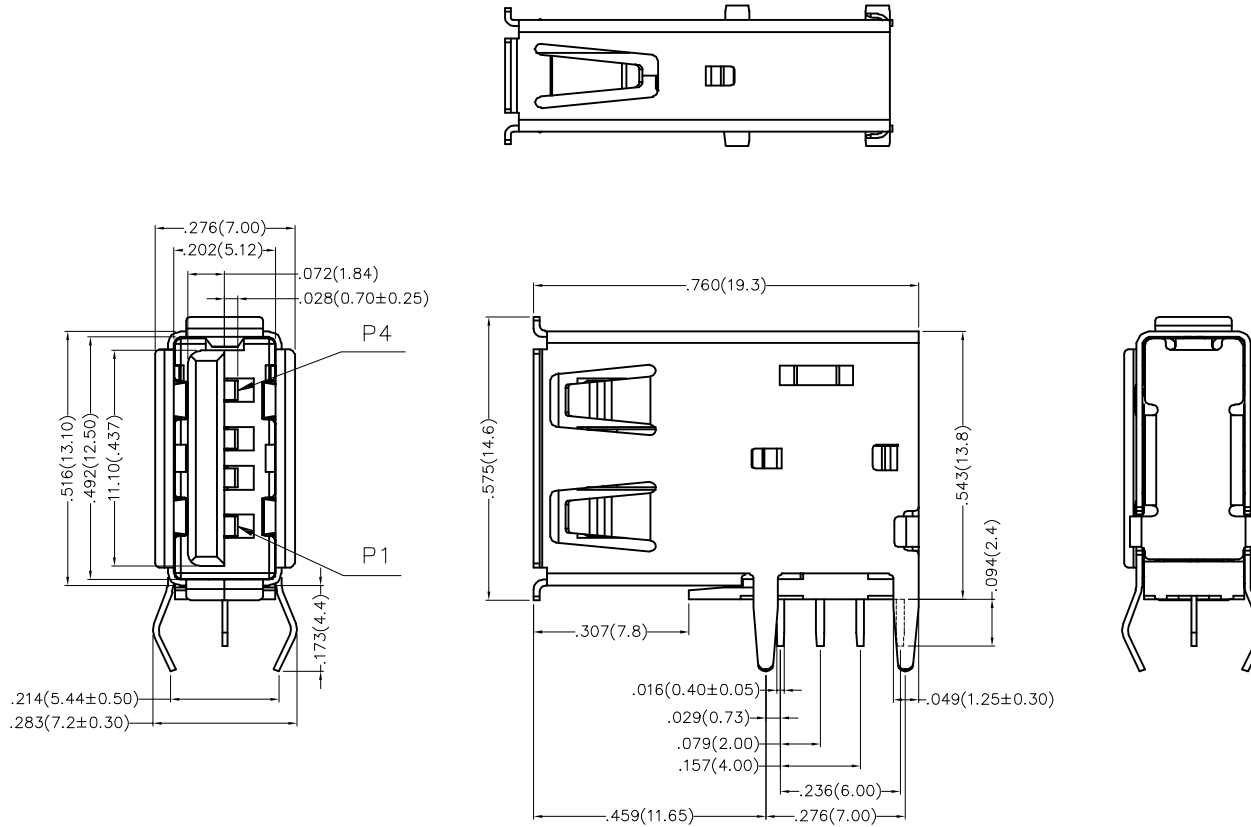
Housing: PBT (UL 94V-0) , White
Bottom cover: PBT (UL 94V-0) , White ▲
Terminal: Phosphor bronze
Shell: Brass
Rear shell: SPCC

Plating

Terminal: Gold in contact area, tin on tail
Shell: Nickel plated
Rear shell: Nickel plated

Pinassignment:

Circuits	Signal name
1	VBUS
2	D-
3	D+
4	GND



Recommended P.C.Board Layout

<p>THIRD ANGLE PROJECTION</p>	GENERAL TOLERANCES (UNLESS SPECIFIED)		APPROVE BY	DATE	PART NO.	ITEM NO.	<p>Building Technology Cornerstone</p>
	X.±.012(0.30)	X.±5'	FRANK	10/DEC/17	FUS272-FDBWIK	FUS272	
	X.X±.008(0.20)	.X'±2"	CHECKED BY	DATE	TITLE		
DESIGN UNITS	X.XX±.006(0.15)	.XX'±1'	CHERRY	10/DEC/17	USB A-type		REV 1
Inch (metric)	X.XXX±.004(0.10)	.XXX'±0.5'	DRAWN BY	DATE	4Circuits 90°Angle Female(DIP)		SHEET NO. 1/1
SCALE			KATE	10/DEC/17	THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO TXGA INDUSTRIAL ELECTRONICS(S.Z)CO.,LTD AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION		
5:1							
SIZE							
A4							